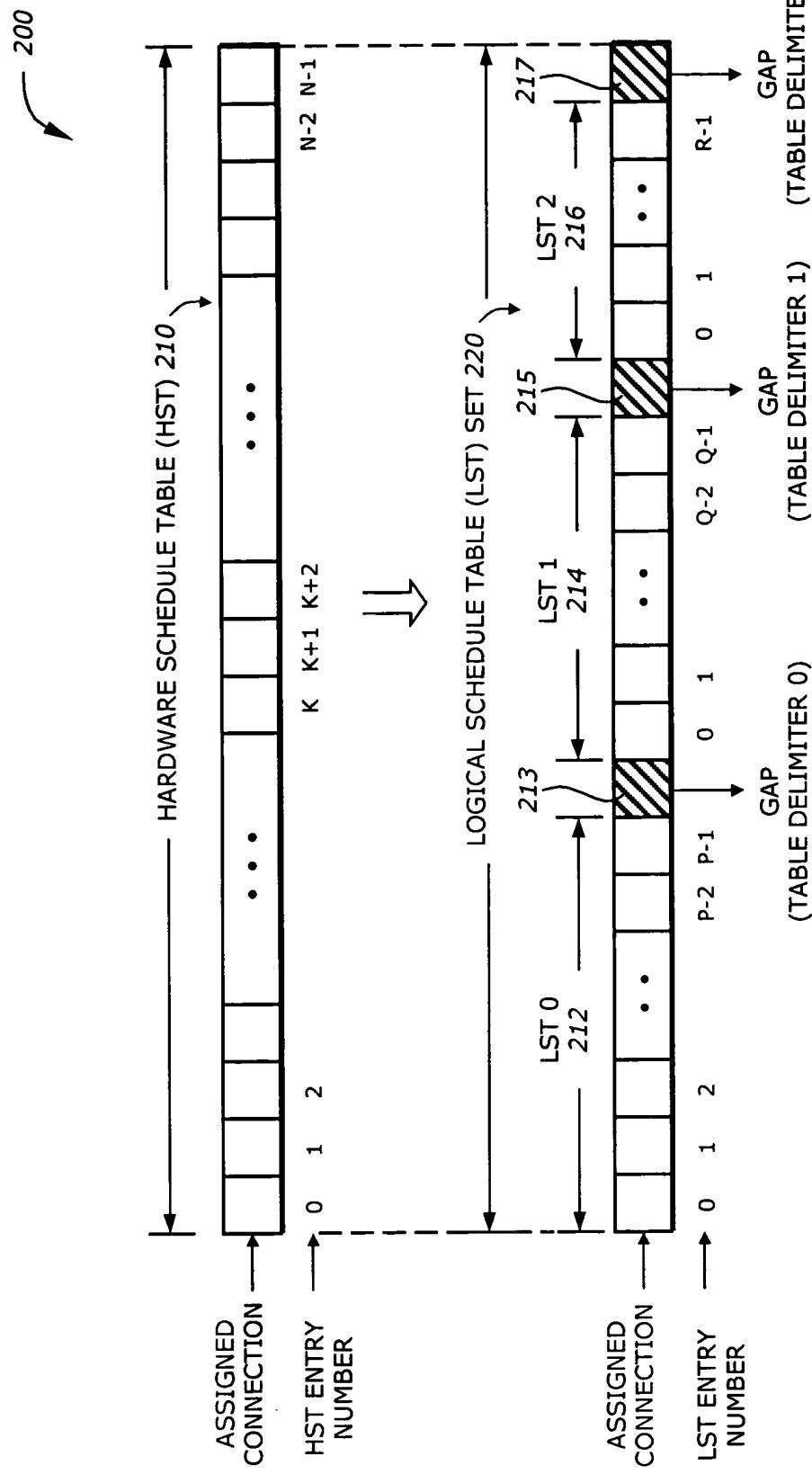


FIG. 1

FIG. 2



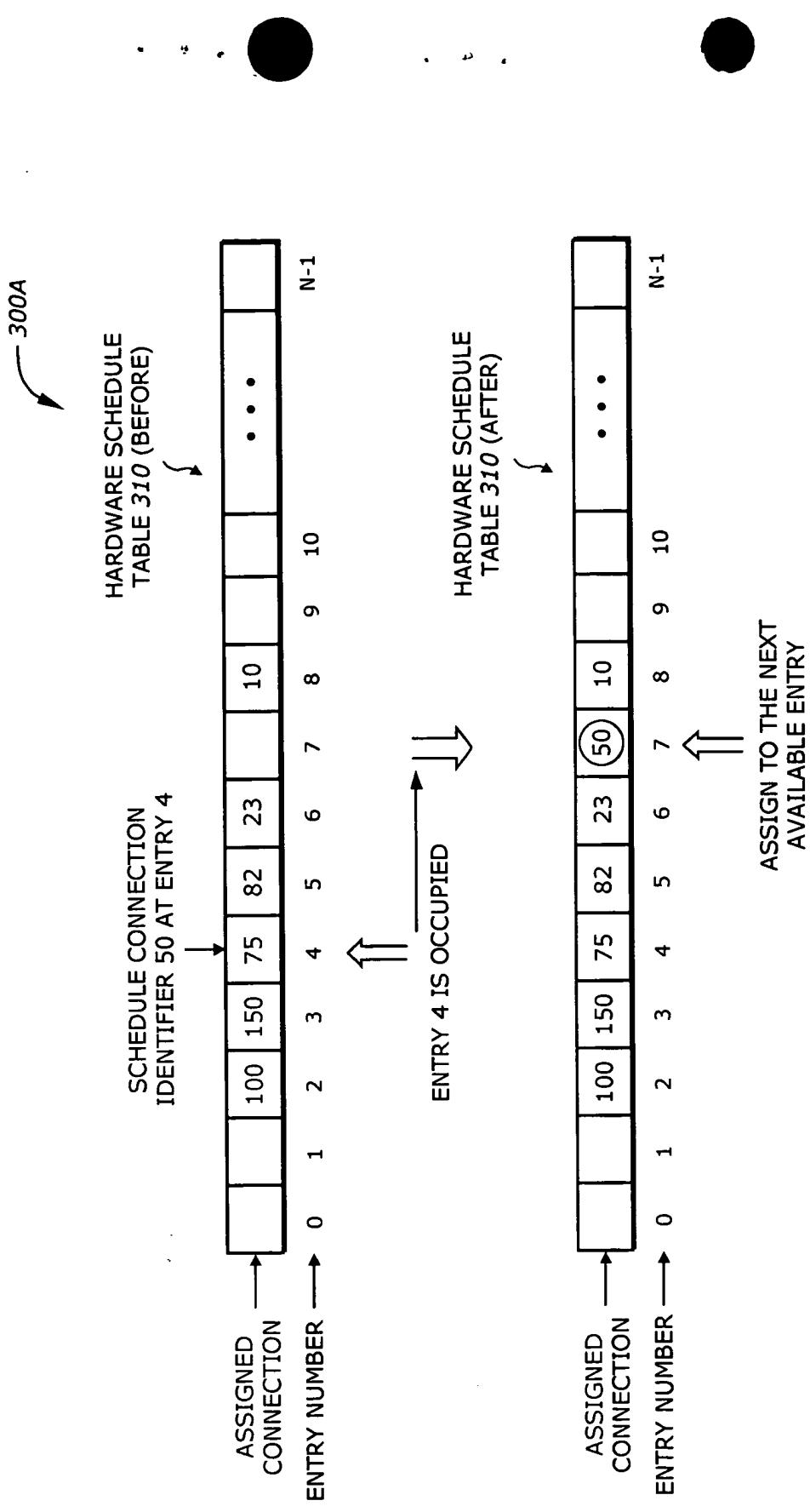


FIG. 3A

300B

HARDWARE SCHEDULE TABLE 320 (BEFORE)

ASSIGNED CONNECTION	125	171	110	82	• • •	14	13	56	92	46	118
ENTRY NUMBER	0	1	2	3	4	N-7	N-6	N-5	N-4	N-3	N-2 N-1

SCHEDULE CONNECTION
CHANNEL 50 AT ENTRY N-3

↓

↑
ENTRY N-3 IS OCCUPIED

HARDWARE SCHEDULE TABLE 320 (AFTER)

ASSIGNED CONNECTION	125	171	110	50	82	• • •	14	13	56	92	46	118
ENTRY NUMBER	0	1	2	3	4	N-7	N-6	N-5	N-4	N-3	N-2	N-1

SCHEDULE IDENTIFIER 50 AT THE
NEXT AVAILABLE ENTRY BY WRAPPING
AROUND PAST (N-1)TH ENTRY.

↓

↓

FIG. 3B

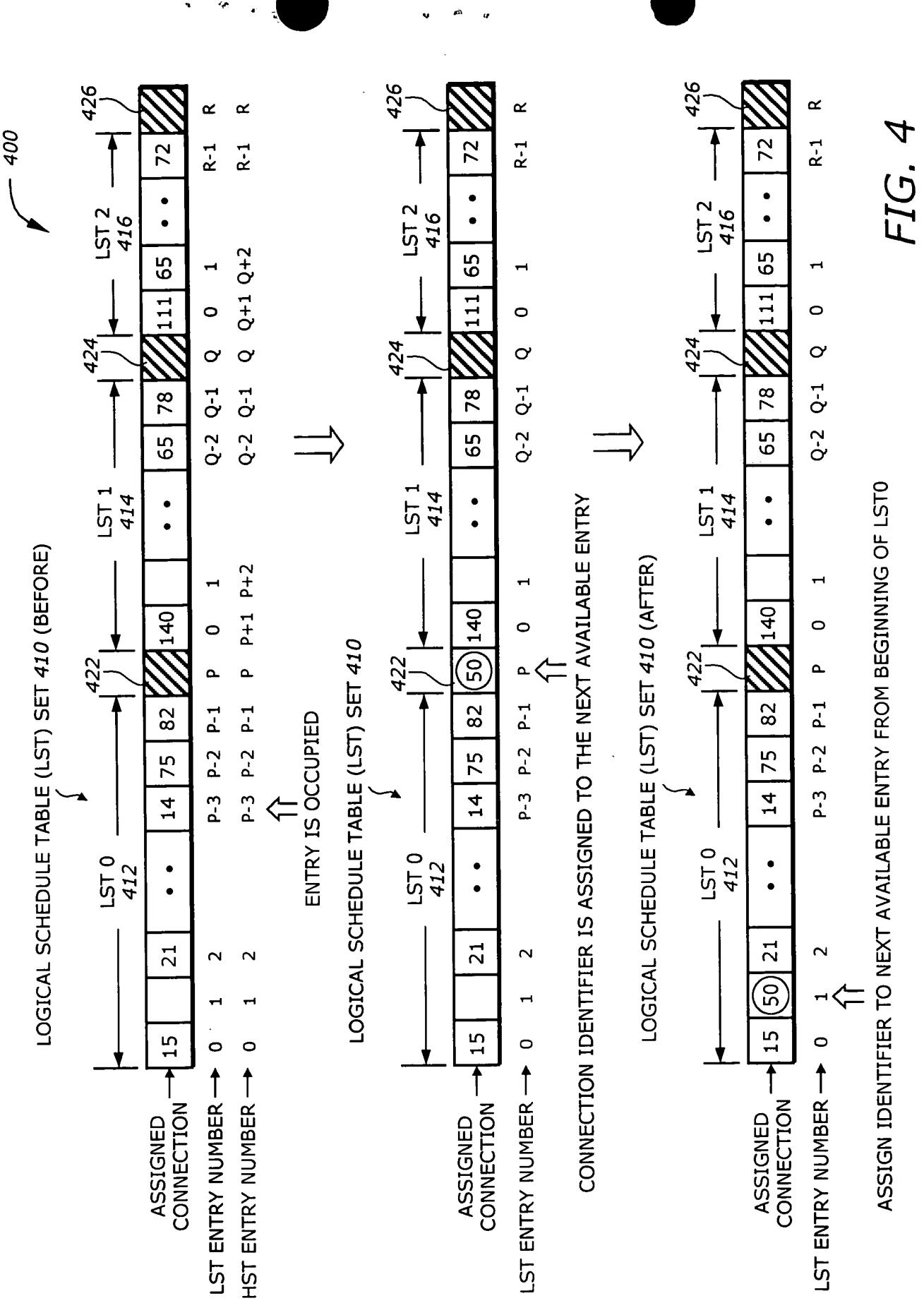
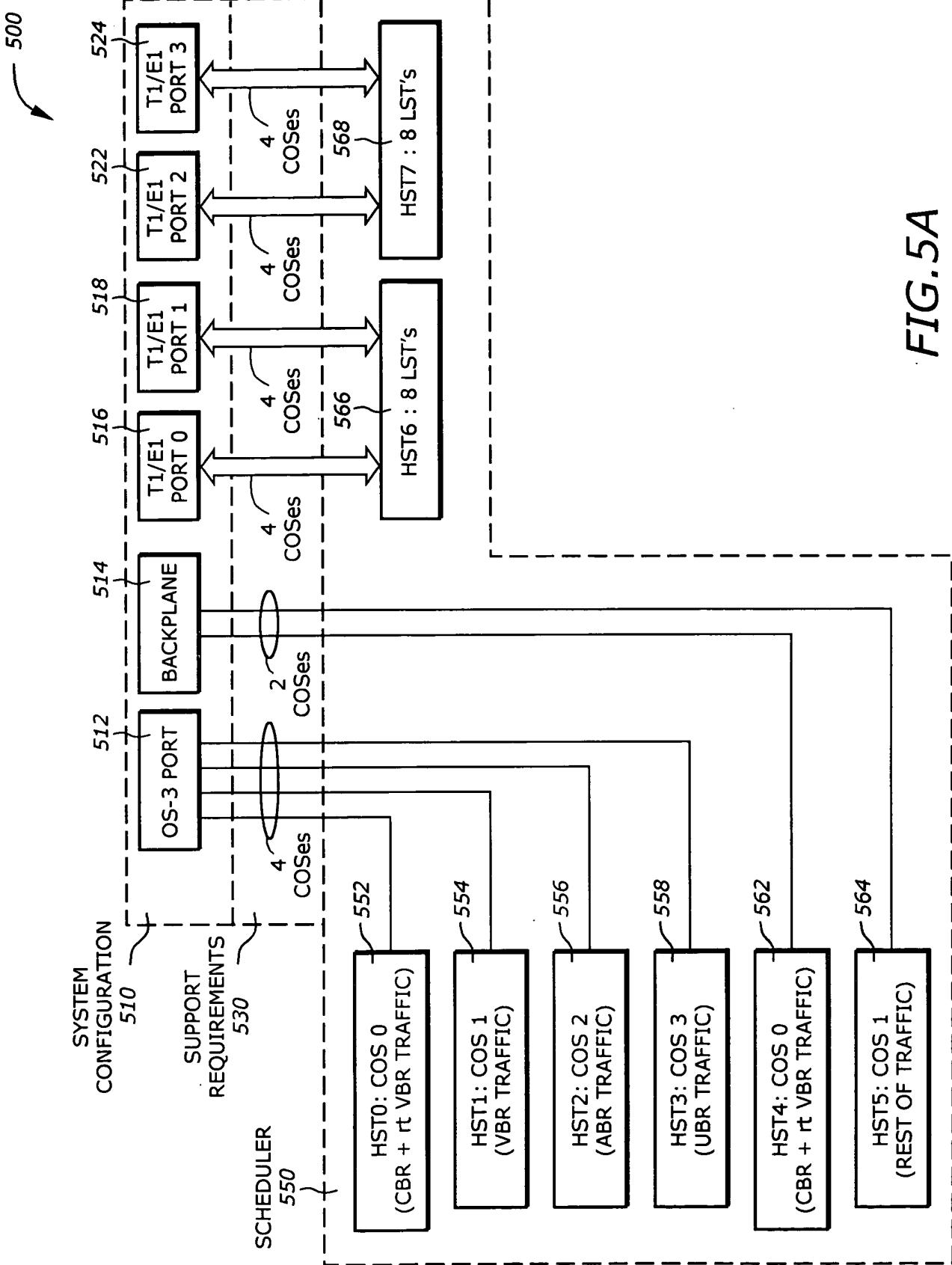
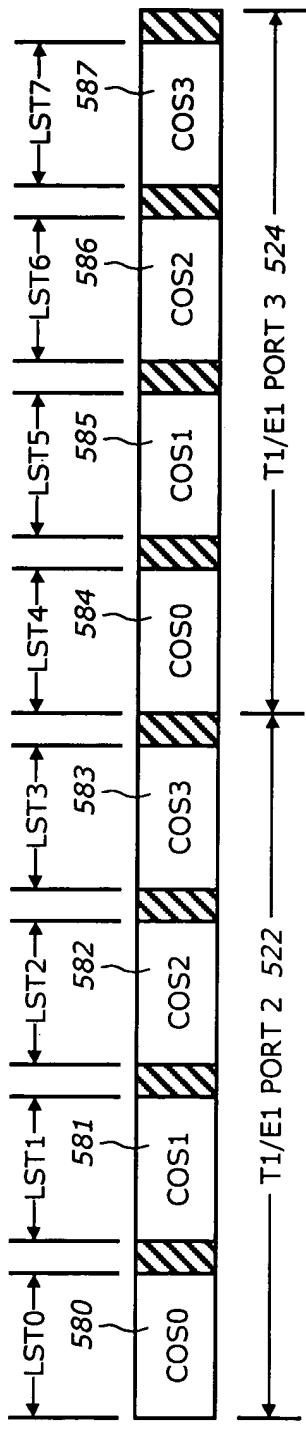


FIG. 5A



LOGICAL SCHEDULE TABLE SET 568



: GAP OR DELIMITER

LOGICAL SCHEDULE TABLE SET 566

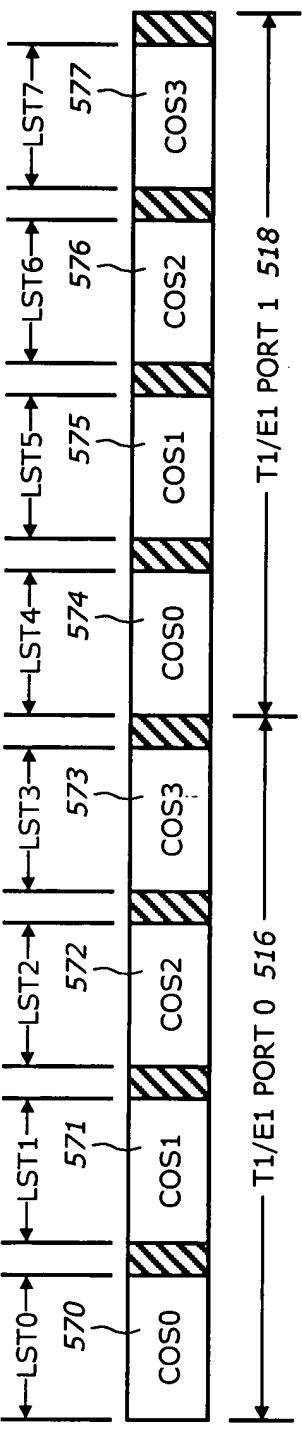


FIG. 5B

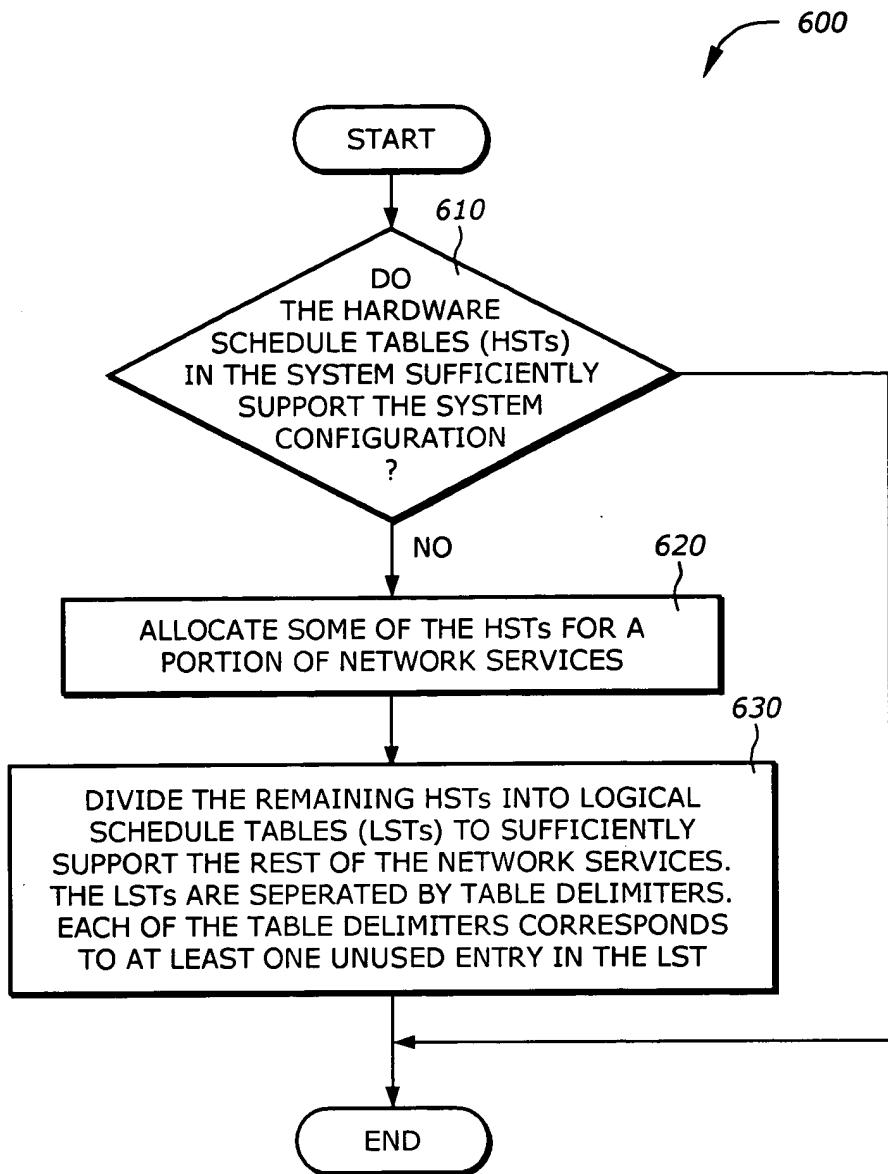


FIG. 6

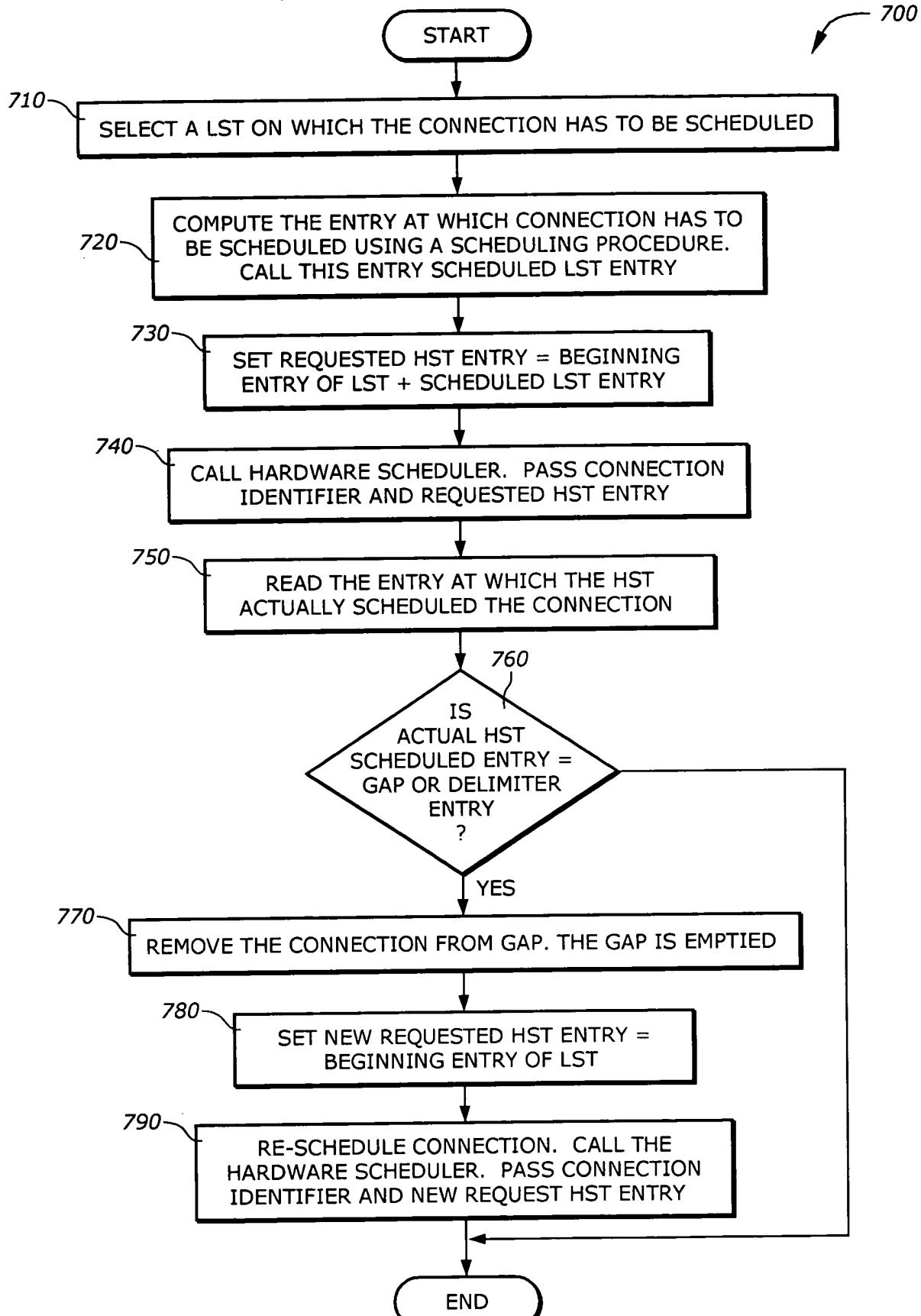


FIG. 7